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<u>L5</u>	L3 or L4	21	<u>L5</u>
<u>L4</u>	L2 and ((card or board) near10 (transaction or job or task))	21	<u>L4</u>
<u>L3</u>	L2 and (prevent\$3 near5 (transaction or job or task))	3	<u>L3</u>
<u>L2</u>	((board or card) near10 ((hot or live) adj1 (swap\$4 or remov\$3 or extract\$3))) same controller	123	<u>L2</u>
<u>L1</u>	(board or card) same ((hot or live) adj1 (swap\$4 or remov\$3 or extract\$3)) same controller	200	<u>L1</u>

END OF SEARCH HISTORY

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L5	0

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<u>L6</u>	L5	0	<u>L6</u>
	<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L5</u>	l3 or L4	21	<u>L5</u>
<u>L4</u>	L2 and ((card or board) near10 (transaction or job or task))	21	<u>L4</u>
<u>L3</u>	L2 and (prevent\$3 near5 (transaction or job or task))	3	<u>L3</u>
<u>L2</u>	((board or card) near10 ((hot or live) adj1 (swap\$4 or remov\$3 or extract\$3))) same controller	123	<u>L2</u>
<u>L1</u>	(board or card) same ((hot or live) adj1 (swap\$4 or remov\$3 or extract\$3)) same controller	200	<u>L1</u>

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts

Pending

Active

L1: (105) (board or card)

L2: (2) 11 and (prevent

L3: (22) 11 and ((board

L4: (13) 11 and ((board

L5: (13) 12 or 14

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search

Lists

Preview

Queue

Clear

DBs

USPAT

Default operator

OR

Plurals

Highlight all hit terms initially

BRS form

IS&R form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment	Error	Definit	Er
1	BRS	L1	105	(board or card) same	USPA	2005/10/1				
				((hot or live) adj1 (T	2 14:55				
2	BRS	L2	2	11 and (prevent\$3	USPA	2005/10/1				
				near5 (transaction or	T	2 14:56				
3	BRS	L3	22	11 and ((board or	USPA	2005/10/1				
				card) same (transacti	T	2 14:57				
4	BRS	L4	13	11 and ((board or	USPA	2005/10/1				
				card) near10 (transac	T	2 14:57				
5	BRS	L5	13	12 or 14	USPA	2005/10/1				
					T	2 14:58				

EAST - [Untitled1:1]

File View Edit Tools Window Help

☐ Drafts
☐ Pending
☒ Active
 L1: (105) (board or car
 L2: (2) 11 and (prevent
 L3: (22) 11 and ((board
 L4: (13) 11 and ((board
 L5: (13) 12 or 14
☐ Failed
☐ Saved
☐ Favorites
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 Default operator: **OR** ☒ Highlight all hit terms initially

12 or 14

☒ BRS form ☒ IS&P form ☒ Image ☒ Text ☒ HTML

	U	I	Document ID	Issue Dat	Pages	Title	Current OR	Current X
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6952785 B1	20051004	20	Methods and apparatus for powering a data com	713/300	370/244; 370/271;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6950896 B2	20050927	10	Hot swap compact PCI power supply	710/301	710/302; 713/300
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6925516 B2	20050802	28	System and method for providing an improved c	710/301	361/736; 710/107;
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6898278 B1	20050524	22	Signaling switch for use in information prot	379/325	379/229
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6795902 B2	20040921	19	Inter-domain data transfer	711/153	709/213; 709/238;
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6708283 B1	20040316	17	System and method for operating a svstem with	714/5	710/316; 714/43
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6658507 B1	20031202	11	System and method for hot insertion of comput	710/100	710/301; 710/302;
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6571360 B1	20030527	15	Cage for dynamic attach testing of I/O boards	714/44	714/40
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6487624 B1	20021126	16	Method and apparatus for hot swapping and bu	710/302	307/141; 307/64;
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6452789 B1	20020917	9	Packaging architecture for 32 processor server	361/683	174/158R; 361/727;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6308887	20011030	12	Multi-transactional	235/379	902/4



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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

- ☐
1. Hot swap controller and its control strategy design
 Zhang Danyan; Wu Xiaobo; Zhao Menglian; Yan Xiaolang;
 Solid-State and Integrated Circuits Technology, 2004. Proceedings. 7th International Conference on
 Volume 2, 18-21 Oct. 2004 Page(s):1579 - 1582 vol.2
 Digital Object Identifier 10.1109/ICSICT.2004.1436923
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- ☐
2. Exploring the options for distributed and point of load power in telecomm and network applications
 Deuty, S.;
 Telecommunications Energy Conference, 2004. INTELEC 2004. 26th Annual International
 19-23 Sept. 2004 Page(s):223 - 229
 Digital Object Identifier 10.1109/INTELEC.2004.1401470
[AbstractPlus](#) | Full Text: [PDF](#)(721 KB) IEEE CNF
- ☐
3. Challenges in the packaging of an eight way server
 Aldridge, T.V.;
 Electrical Performance of Electronic Packaging, 1999
 25-27 Oct. 1999 Page(s):9
 Digital Object Identifier 10.1109/EPEP.1999.819182
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- ☐
4. AMP Z-pack 2 mm HM connectors with quiet mate contacts for resolution of nanosecond discontinuity in hot-swap applications
 Demirci, H.H.; Laub, M.; Fry, C.;
 Electronic Components and Technology Conference, 2001. Proceedings., 51st
 29 May-1 June 2001 Page(s):1239 - 1244
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Hot swap controller and its control strategy design

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Abstract

A **hot swap** controller was developed to ensure the safe insertion and removal of a circuit **board** from a live backplane. Multi protections of both **board** and backplane were proposed and realized in this design, which includes automatically starting current limiting or circuit breaking in the event of over current faults and driving an SCR crowbar to protect the loads in the event of over input voltage faults. In addition, the under-voltage detecting and output voltage monitoring is also available for its use. A lot of efforts were put into the design and optimization of its control strategy since the performance of the circuit depends it strongly. The simulation results are well consistent with the expectations. This IC could be realized in BCD technology (Bipolar-CMOS-DMOS technology).

Index Terms

Inspe

Controlled Indexing

CMOS integrated circuits, overcurrent protection, overvoltage protection, power supply circuits, printed circuit accessories, thyristors

Non-controlled Indexing

bipolar-CMOS-DMOS technology, circuit board insertion, circuit board removal, circuit breaking, circuit limiting, control strategy design, hot swap controller, live backplane, over-current protection, over-voltage protection, silicon controlled rectifier

Author Keywords

Not Available

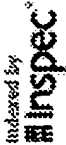
References

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